Inventor:

Zachary B. Katz

Title:

Methods of Forming Planarized Surfaces Over Semiconductor

Substrates

Assignee:

Micron Technology, Inc.

## INFORMATION DISCLOSURE STATEMENT

References - See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR § 1.56. Copies of the cited art are included with the exception of U.S. patents and published U.S. applications (1276 OG 55). No admission is made regarding whether all the submitted references are prior art.

Respectfully submitted,

Dated:

JANUARY 26, 2004

By:

David G Latwesen, Ph.D.

Reg. No. 38,533

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				U.S. PATENT DOCUMENTS						
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	AA	6.649,503	1/18/03	Kim et at.						
	AB	6.635.5\$6	10/21/03	Goo et al.						
	AC	6.596.607	7/22/03	Ahn						
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	AF	6.432,843	8/13/02	Kim et al.	al.					
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	AH	6.184.143	2/6/01	Ohashi et al.						
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*Examiner Initial		Document Number		Date	Name		Class	Subclass		Filing Date If Appropriate	
	AA	6.258.649		7/10/01	Nakamura et al.	et al.					
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	AG	AG 5,618,381 4/8/97 Doan et al.									
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